

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : Unassigned  
Applicant : Ruban Kanapathippillai, et al.  
Filed : 08/21/2003  
Title : MEMORY WITH MEMORY CLUSTERS FOR POWER REDUCTION  
IN AN INTEGRATED CIRCUIT (As Amended)  
TC/A.U. : Unassigned  
Examiner : Unassigned  
  
Docket No. : 42P14037D3  
Customer No. : 8791

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Which is a Divisional of:

Appl. No. : 10/109,826 Confirmation No. 5495  
Applicant : Ruban Kanapathippillai, et al.  
Filed : 03/29/2002  
Title : METHOD AND APPARATUS FOR POWER REDUCTION IN A  
DIGITAL SIGNAL PROCESSOR INTEGRATED CIRCUIT  
TC/A.U. : 2181  
Examiner : Richard S. Kim

**PRELIMINARY AMENDMENT  
ACCOMPANYING**

**35 USC 120 and 37 CFR 1.53(b) (1) DIVISIONAL APPLICATION**

Mail Stop New Application  
Commissioner for Patents  
Alexandria, VA 22313

Dear Sir:

Prior to a calculation of fees and a first examination in  
the 35 USC 120 and 37 CFR 1.53(b) (1) divisional application  
filed herewith, please enter the following amendments and  
remarks:

Amendments to the TITLE begin on page 2.

Amendments to the SPECIFICATION begin on page 3.

Amendments to the CLAIMS begin on page 4.

REMARKS begin on page 9.

CONCLUSION and signature is on page 14.

**IN THE TITLE**

Please amend the title as follows:

"~~METHOD AND APPARATUS~~ MEMORY WITH MEMORY CLUSTERS FOR POWER  
REDUCTION IN A ~~DIGITAL SIGNAL PROCESSOR~~ AN INTEGRATED CIRCUIT"